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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Manjeri Krishnan, et al.

TI-34026

Art Unit: 2824

Serial No.: 10/630,963

Examiner: Tuan T. Nguyen

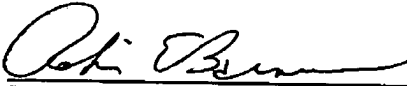
Filed: July 30, 2003

Conf. No.: 1767

For: Electrical Fuse Control of Memory Slowdown

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:


Robin E. Barnum

November 5, 2004
Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT <u>111/Election (6 Pages)</u>
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT <u>(# Page)</u>
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
<input type="checkbox"/> ASSIGNMENT (# Pages)	<input type="checkbox"/> APPEAL <u>(# Pages)</u>
<input type="checkbox"/> FORMAL DRAWINGS	<input type="checkbox"/> ISSUE FEE (# Pages)
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Manjeri Krishnan, et al.	
TITLE OF INVENTION: Electrical Fuse Control of Memory Slowdown	
TI FILE NO.: TI-34026	DEPOSIT ACCT. NO.: 20-0668
RECEIPT DATE & SERIAL NO.: Serial No.: 10/630,963 Filing Date: July 30, 2003	
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Texas Instruments Incorporated
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kirshnan et al

Art Unit: 2824

Serial No.: 10/630,963

Examiner: Tuan T. NGuyen

Filed: July 30, 2003

Docket: TI-34026

For: ELECTRICAL FUSE CONTROL OF MEMORY SLOWDOWN

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AMENDMENT UNDER 37 C.F.R. 1.111

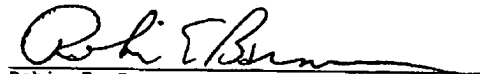
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UNDER 37 C.F.R. §1.6(b)

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Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated October 5, 2004. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: